# Push-Pull on Graphs is Column- and Row-based SpMV Plus Masks

# Objectives

- Investigate the generalizability of direction-optimized BFS.
- Investigate how direction-optimized BFS can be expressed using linear algebra.

### Introduction

Push-pull, also known as direction-optimized breadth-first-search (DOBFS), is a key optimization for making breadth-first-search (BFS) run efficiently. Linear algebra-based frameworks have advantages in conciseness, performance and portability. However, there is no work in literature describing how to implement it within a linear algebra-based framework. Our work shows that DOBFS fits well within the linear algebra-based framework.

#### **Traversal is Matvec**

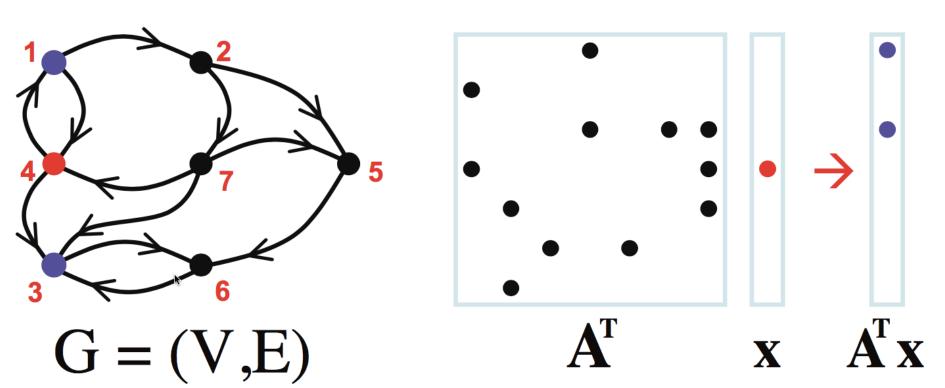


Figure: Matrix-graph duality. The adjacency matrix  $\mathbf{A}$  is the dual of graph G. The matvec is the dual of the BFS graph traversal. Figure is based on Kepner and Gilbert's book.

## **Complexity Results**

Operation		Expected Cost
Row-	unmasked	$\mathcal{O}(dM)$
based	masked	$\mathcal{O}(d \ nnz(\mathbf{m}))$
Column-	unmasked	$\mathcal{O}(d \ nnz(\mathbf{f})\log M)$
based	masked	$\mathcal{O}(d \ nnz(\mathbf{f})\log M)$

Table: Four sparse matvec variants and their associated cost, measured in terms of number of expected memory accesses into the sparse matrix  $\mathbf{A}$  required.

Carl Yang<sup>1,2</sup>, Aydın Buluç<sup>2,3</sup> and John D. Owens<sup>1</sup>

Department of Electrical and Computer Engineering, UC Davis <sup>2</sup> Computational Research Division, LBNL <sup>3</sup> Department of Electrical Engineering and Computer Sciences, UC Berkeley

# **Direction-optimized BFS**

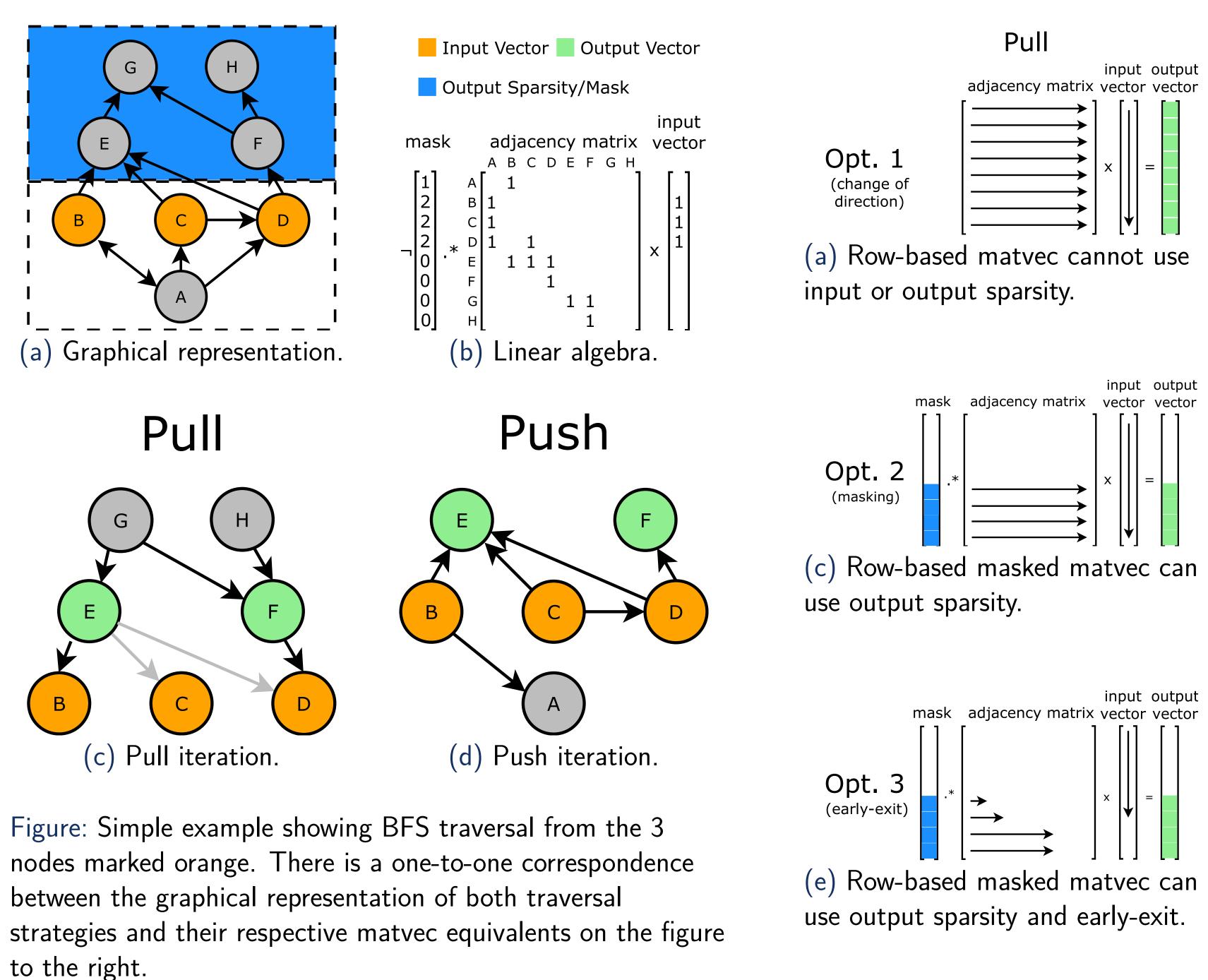


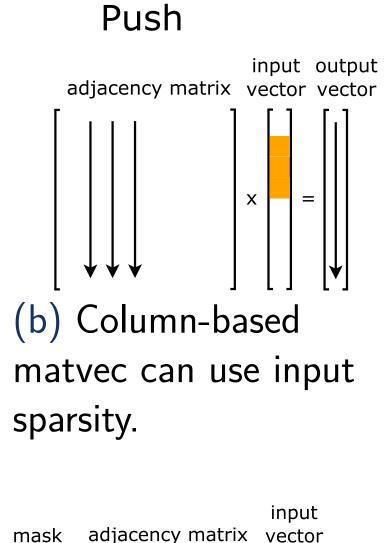


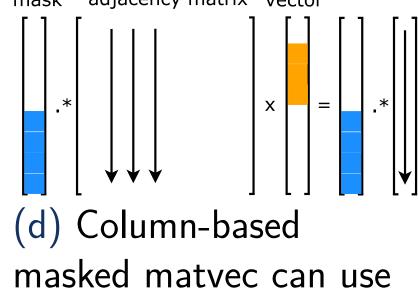
Figure: Comparison of our work to other graph libraries (SuiteSparse, CuSha, a baseline push-based BFS, Ligra, and Gunrock) implemented on  $1 \times$  Intel Xeon 4-core E5-2637 v2 CPU and  $1 \times$  NVIDIA Tesla K40c GPU.

#### **Experimental Results**

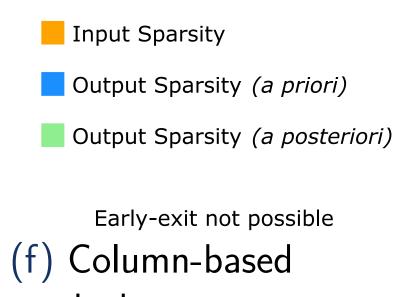
# Optimizations

Figure: The three optimizations known as "direction-optimized" BFS.



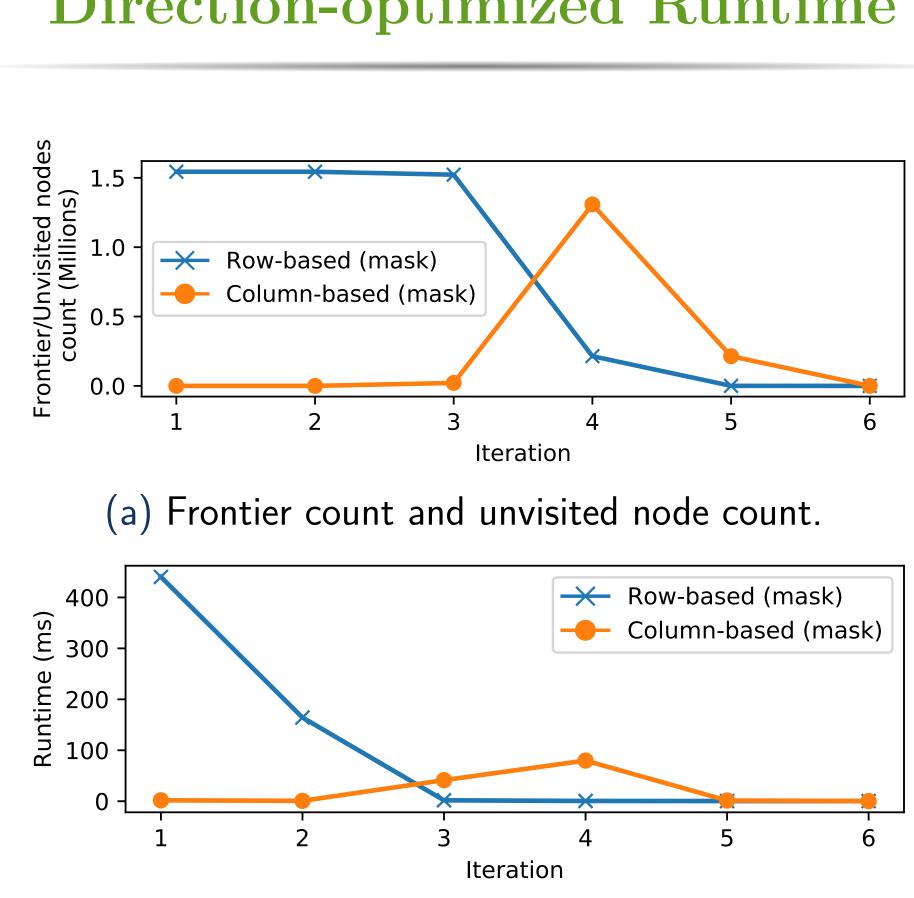


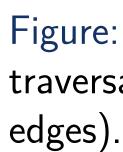
input sparsity.



masked matvec cannot early-exit.

In this paper we demonstrate that push-pull corresponds to the concept of column- and row-based masked matvec. A possible future research direction would be to extend masking to other applications such as triangle counting and enumeration, adaptive PageRank, batched betweenness centrality, maximal independent set, and convolutional neural networks.









# Conclusion

#### **Direction-optimized Runtime**

(b) Push and pull runtime.

Figure: Breakdown of edge types in frontier during BFS traversal of Kronecker scale-21 graph (2M vertices, 182M)

# **Contact Information**

• Web: http://www.ece.ucdavis.edu/~ctcyang/ • Email: ctcyang@ucdavis.edu • Phone: +1 (916) 802-8178